



28/2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Chin-Chi TENG et al

Art Unit: 2812

Application No: 10/043,458

Examiner:
Stacy Whitmore

Filed: January 9, 2002

For: CLOCK TREE SYNTHESIS FOR A
HIERARCHICALLY PARTITIONED IC LAYOUT

#8/A
W/
DW95
J: 95
94/10

REPLY TO THE OFFICE ACTION MAILED 03/24/2003

COMMISSIONER FOR PATENTS
P.O. Box 1450 /
Alexandria, Virginia 22313-1450

Sir:

Further examination and consideration of this application are
requested in view of the following Amendments and Remarks.

RECEIVED
JUN 17 2003
TECHNOLOGY CENTER 2800